

IN THE SPECIFICATION.

Please amend the Specification as follows:

Page 5, first and second full paragraphs should read as follows:

Further, in general, it is known that when a "MASH" is formed by connecting a plurality of delta sigma circuits, noise characteristics is improved in the delta sigma configuration (see, for example, Non-Patent Document 2).

Patent Document 1: ~~JP-A No. 2000-052044~~ JP-A No. 2002-152044

Patent Document 2: JP-A No. H05-500894

Non-Patent Document 1: The Institute of Electronics, Information and Communication Engineers, Transactions C-1, Vol. J76-C-1, No. 11, pp. 445-452, November 1993, "A High-Speed Frequency Switching Synthesizer Using ~~Fraction Division Method~~ Fractional N Phase Locked Loop".